

	Class	Subjects
ISSUE CLASSIFICATION		

Q.I.P.E. SCANNED <i>SB2</i> Q.A. <i>LR</i>	PATENT DATE
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APPLICATION NO. 09/915761	CONT/PRIOR F	CLASS 2142	SUBCLASS	ART UNIT 2183	EXAMINER O'Brien
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TITLE **APPLICANTS**

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Digital signal processor with parallel architecture

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div> <div>_____</div> <div>(Assistant Examiner)</div> <div>_____</div> <div>(Date)</div> </div>		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div>_____</div> <div>_____</div>	<div> <div>_____</div> <div>(Primary Examiner)</div> <div>_____</div> <div>(Date)</div> </div>		ISSUE FEE	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<div> <div>_____</div> <div>(Legal Instruments Examiner)</div> <div>_____</div> <div>(Date)</div> </div>		ISSUE BATCH NUMBER	

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